Abstracts

An Integrated 18.75/37.5 GHz FET Frequency Doubler

S. Meszaros, C.J. Verver, R.J.P. Douville and W.J.R. Hoefer. "An Integrated 18.75/37.5 GHz FET Frequency Doubler." 1988 MTT-S International Microwave Symposium Digest 88.2 (1988 Vol. II [MWSYM]): 815-818.

The design and performance of an 18.75/37.5 GHz FET frequency doubler is presented. The doubler is implemented in a combination of antipodal finline and suspended microstrip, permitting the entire unit to be integrated on a single substrate. The resulting circuit is a particularly simple and cost-effective component for low and medium power applications such as local oscillators. The doubler was built using a NEC673 FET and has a conversion loss of 5.8 dB over a 350 MHz input bandwidth.

❑ Return to main document.